

COMMUNICATION AVAILABLE: CH-a,CH-b,···CH-k
COMMUNICATION UNAVAILABLE: CH-k+1,CH-k+2

FIG.2

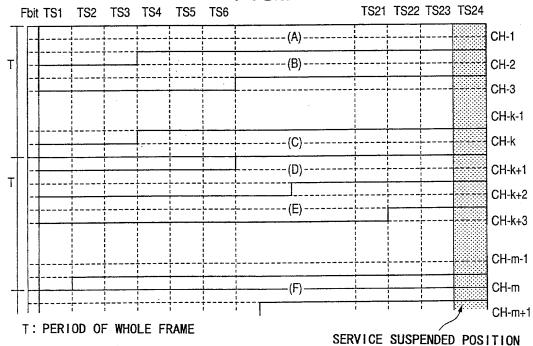
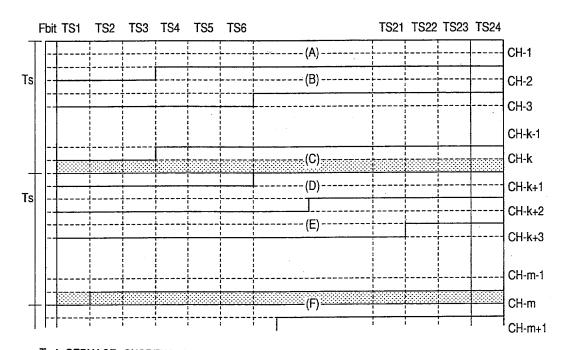
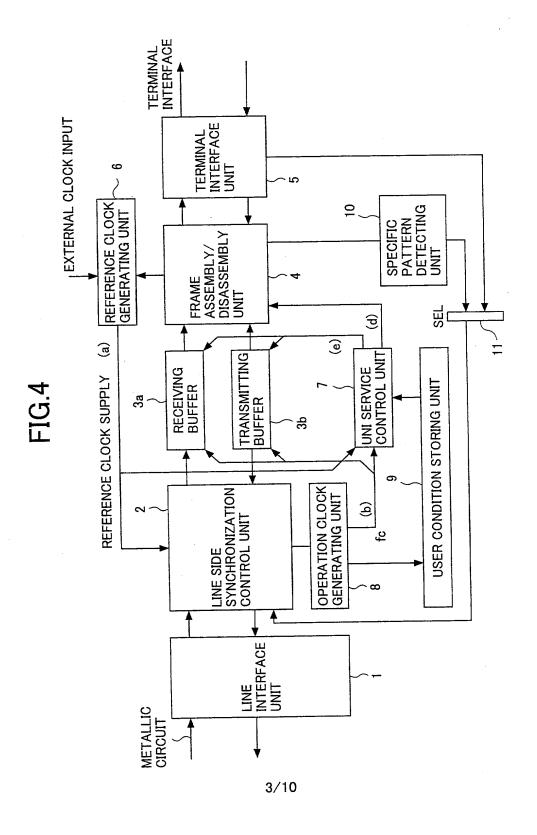


FIG.3



Ts: SERVICE SUSPENSION BIT SETTING PERIOD



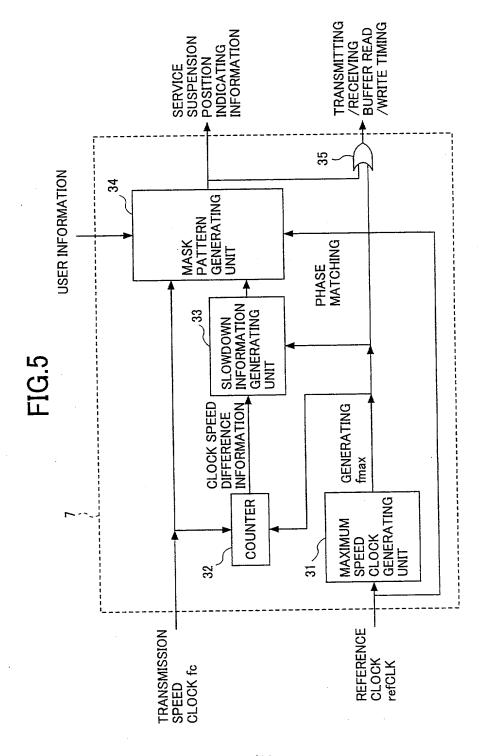
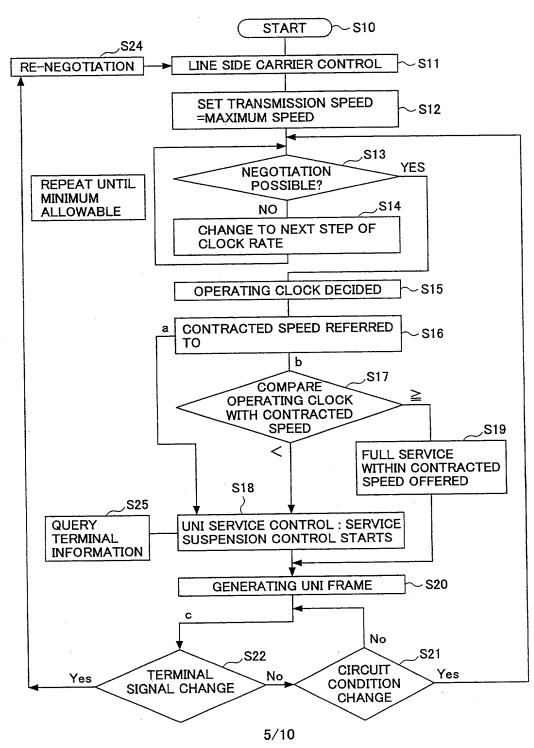
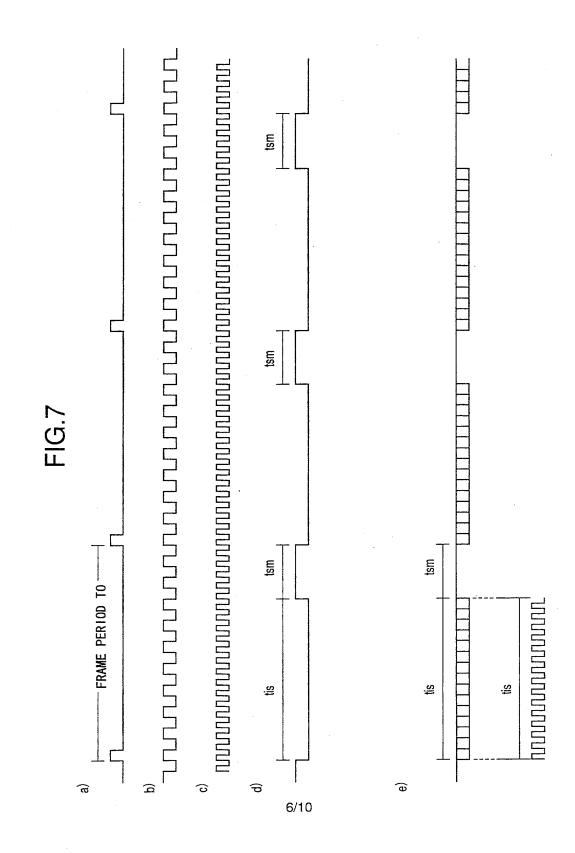


FIG.6





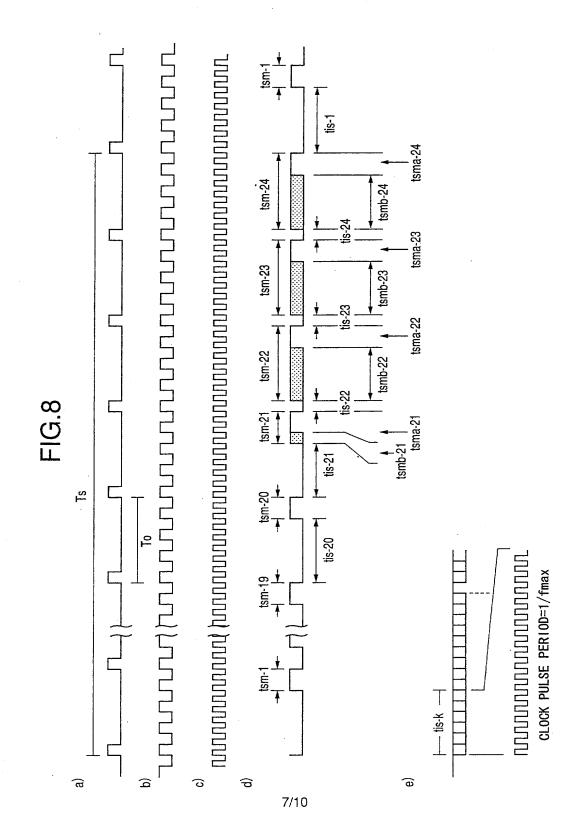


FIG.9

a) WHEN OPERATING AT MAXIMUM SPEED

Fbit							
TS1	TS2	TS3	TS4	$[\cdots]$	TS22	TS23	TS24

b) AT FALL-BACK

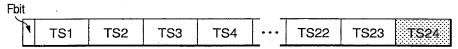


FIG.10

FRAME NUMBER

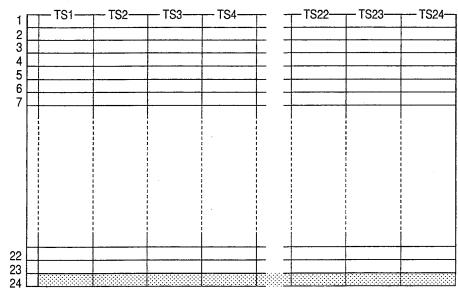
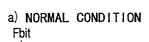


FIG.11



-bit							
1	TS1	TS2	TS3	TS4	 TS17	TS18	TS19~24

b) COMMON FRAME METHOD

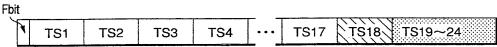


FIG.12

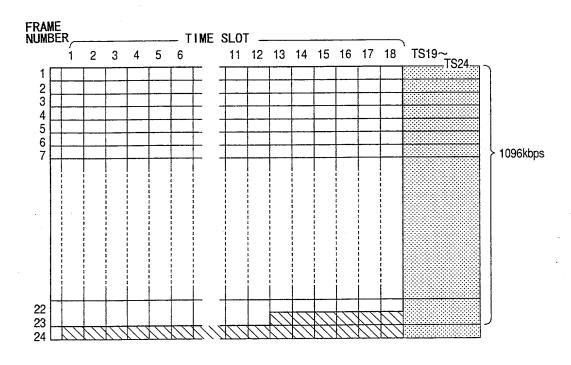


FIG.13

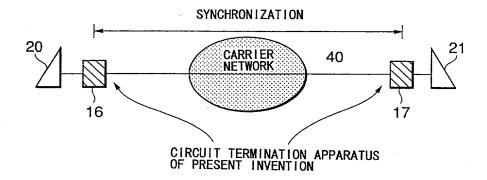


FIG.14

